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Dated:

3/23/05

Signature:

Michael J. Doherty

(Michael J. Doherty)

Docket No.: TESSERA 3.0-273
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Warner et al.

Application No.: 10/699,328

Group Art Unit: 2818

Filed: October 31, 2003

Examiner: Not Yet
Assigned

For: MICROELECTRONIC ASSEMBLIES HAVING
LOW PROFILE CONNECTIONS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This communication is in response to the Office Action mailed February 11, 2005, setting forth a Restriction Requirement in the above-identified application. In the Office Action, the Examiner required restriction to one of the following inventions under 35 U.S.C. §121:

I. Claims 1-43, drawn to a method of making a microelectronic assembly and a method of forming protrusions on a microelectronic element.

II. Claims 42-52, drawn to a semiconductor chip assembly.

In response, Applicants hereby elect the invention of Group I, corresponding to claims 1-43. Applicants reserve the

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Docket No.: TESSERA 3.0-273

right to file a divisional application directed to the non-elected claims.

In the event that any fee is due in connection with the present response, the Examiner is authorized to charge Applicants' Deposit Account No. 12-1095 therefor.

Dated:

Respectfully submitted,

By 
Michael J. Doherty

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